Adam Duncan is the Hardware Assurance Chief Engineer for the Trusted Microelectronics Division (GXV) at NSWC Crane. Adam also serves as the Navy Hardware Assurance (HwA) Action Officer (AO) for the Joint Federated Assurance Center (JFAC) and supports the OUSD(RE) Trusted and Assured Microelectronics (T&AM) Program. Adam received his Ph.D. from Indiana University, his MS from Vanderbilt University, and his BS Degree from the University of Michigan.